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ATTORNEY DOCKET NO. 60188-028  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Toshio YAMADA

Serial No.: 09/779,751

Filed: February 9, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT, )  
COMPUTER SYSTEM, DATA PROCESSOR )  
AND DATA PROCESSING METHOD )

)  
) Group Art Unit: 2127  
)  
) Examiner: LILIAN VO  
)  
)  
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MAY 23 2003

ELECTION UNDER 35 U.S.C. § 121

Technology Center 2100

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the restriction requirement set forth in the Office Action mailed April 22, 2003, having a shortened statutory period for response set to expire May 22, 2003, wherein the Examiner required restriction between:

- Group I - claims 5, 14 and 15, drawn to memory emulation;
- Group II - claims 6-11, 21 and 22, drawn to reading and writing data from the memory emulation;
- Group III - claims 12, 13 and 19-22, drawn to informing what type of data processing function an IC has; and
- Group IV - claim 16, drawn to dynamically changing a relationship between the logical and the physical address.

Applicants elect without traverse, Group II - claims 6-11, 21 and 22, for initial

prosecution on the merits. Please cancel claims 5, 12-16, 19 and 20, without prejudice.

Applicant also reserves the right to file a Divisional Application for the non-elected claims 5, 12-16, 19 and 20, which the Examiner has indicated is patentably distinct.

Applicant believes that no extension of time is required. However, this conditional petition is being made to provide for the possibility that Applicant has inadvertently overlooked the need for a petition for extension of time. The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No. 50-0417. A duplicate copy of this Response is enclosed for accounting purposes.

Respectfully submitted,

McDERMOTT, WILL & EMERY

Date: 5/22/03

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